## Amendments to the Drawings:

The attached replacement sheets of drawings include changes to Figs. 1-3. These sheets replace all previous versions of the drawings, which include: the original sheets filed with the application, the replacement sheets filed on April 1, 2002, and the amended drawings filed on February 28, 2003 along with a previous response.

In Figure 1, the lead line for reference numeral 18 has been corrected to correspond with the originally filed drawings, the outline for nitride layer 40 has been added to correspond with the originally filed drawings, and the term "related art" has been replaced with "prior art" as previously requested by the Office.

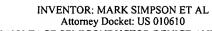
In Figure 2, the outline for region 50 has been added to correspond with the originally filed drawings, the lead lines for reference numerals 50 and 26 have been corrected to correspond with the originally filed drawings, and the term "related art" has been replaced with "prior art" as previously requested by the Office.

In Figure 3, the lead lines for reference numerals 126, 128, and 130 have been corrected to correspond to the originally filed drawings.

Attachments: 3 Replacement Sheets

3 Marked Up Sheets Showing Changes

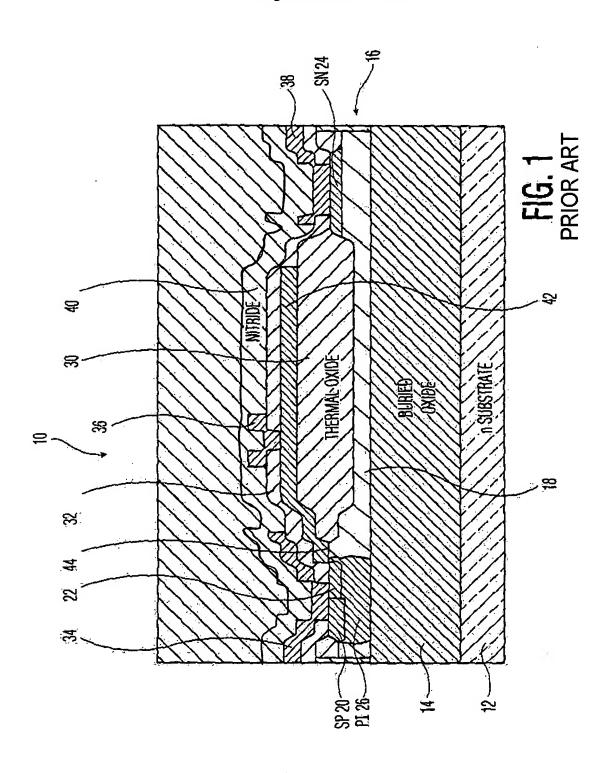
Serial No.: 10/015,847 2 of 10





Title: DUAL GATE OXIDE HIGH-VOLTAGE SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME
Contact: STEVEN R. BIREN 914) 333-9630

1/3
Replacement Sheet





## 2/3 Replacement Sheet

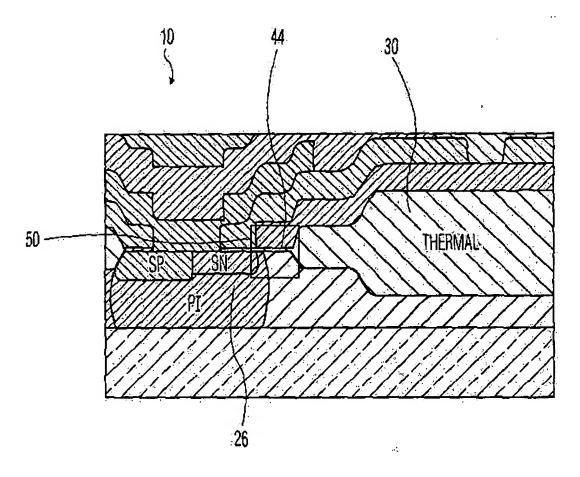


FIG. 2 PRIOR ART

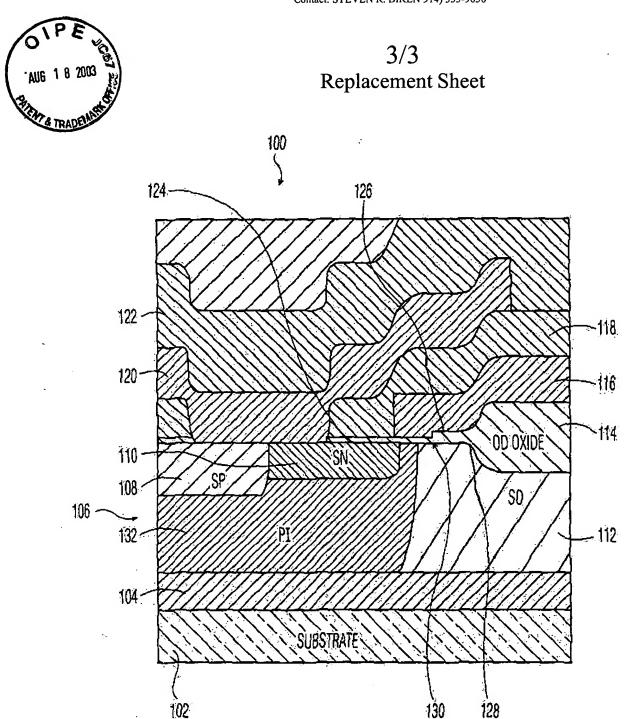
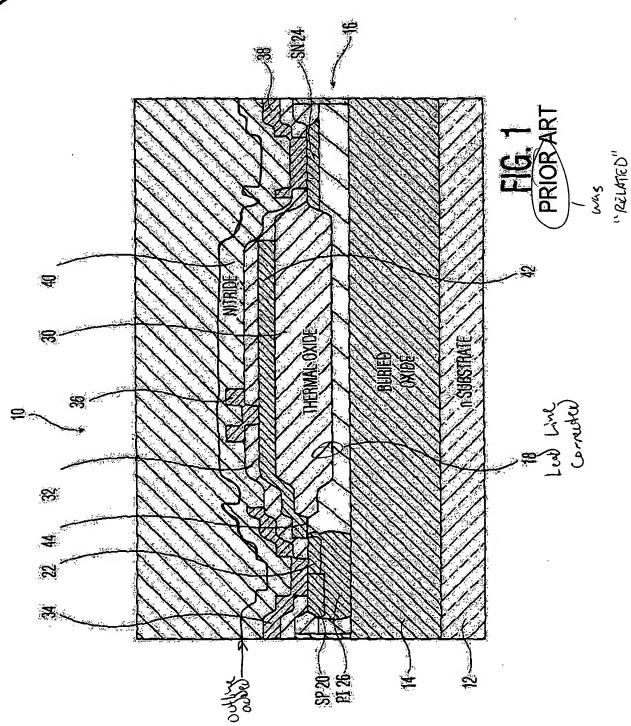


FIG. 3



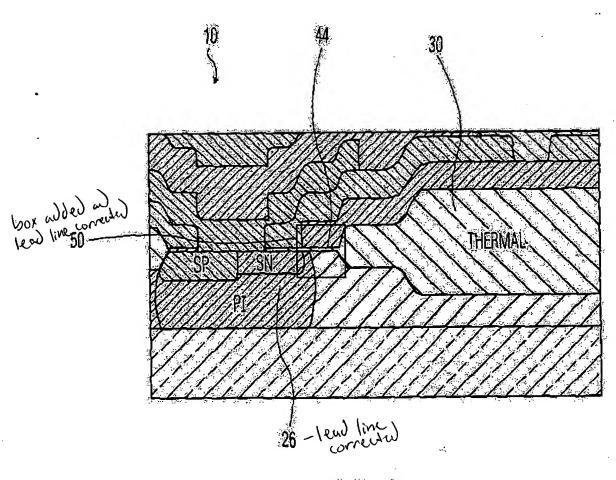
1/3
Markup Replacement Sheet



## INVENTOR: MARK SIMPSON ET AL Attorney Docket: US 010610 Title: DUAL GATE OXIDE HIGH-VOLTAGE SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME Contact: STEVEN R. BIREN 914) 333-9630



2/3
Markof Replacement Sheet



PRIOR ART

Title: DUAL GATE OXIDE HIGH-VOLTAGE SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME Contact: STEVEN R. BIREN 914) 333-9630

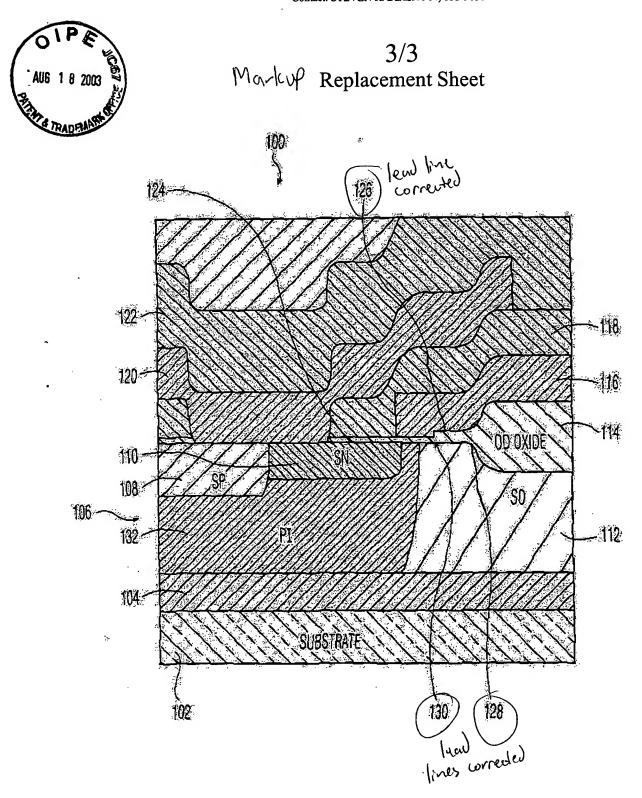


FIG. 3